

FIGURE 1

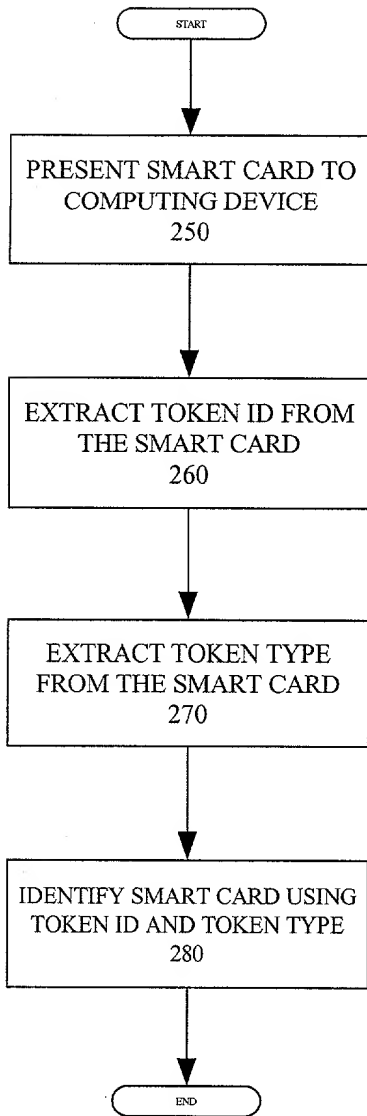


FIGURE 2A

FIG. 2B

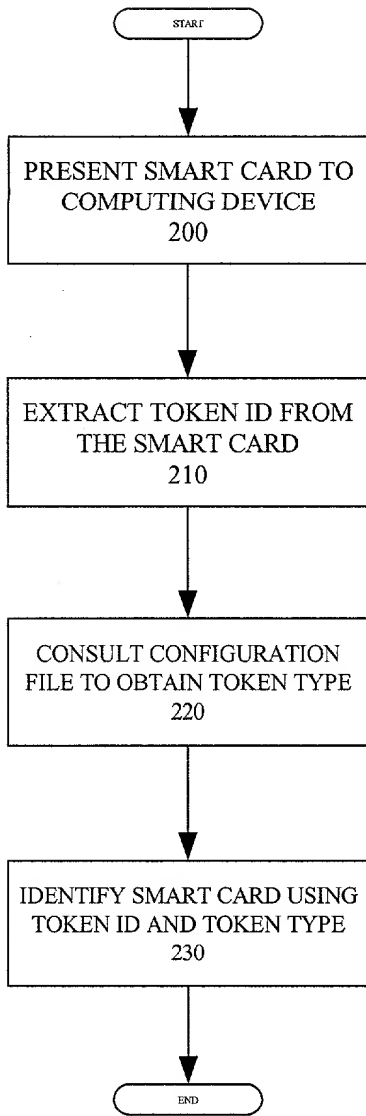


FIGURE 2B

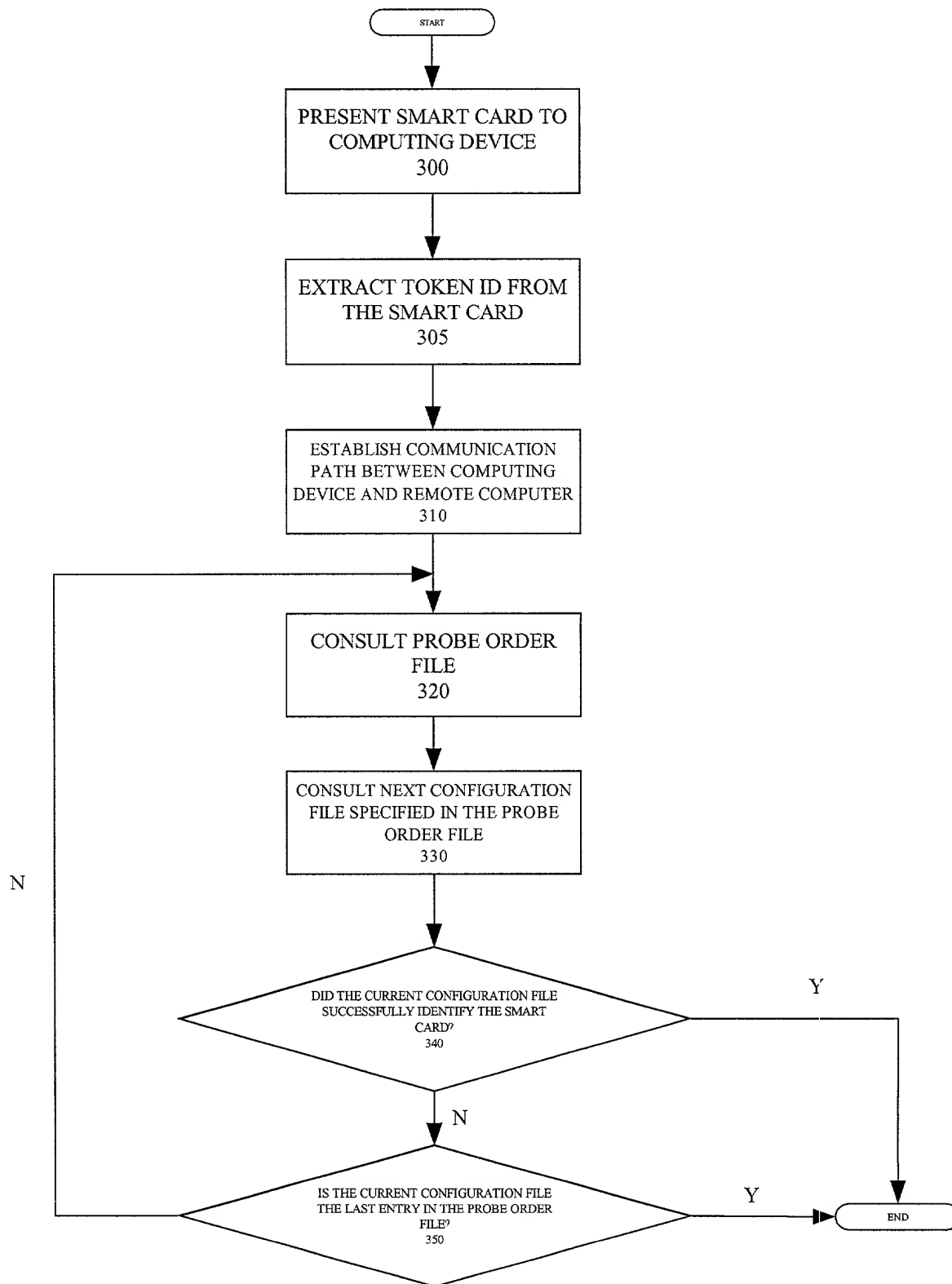


FIGURE 3

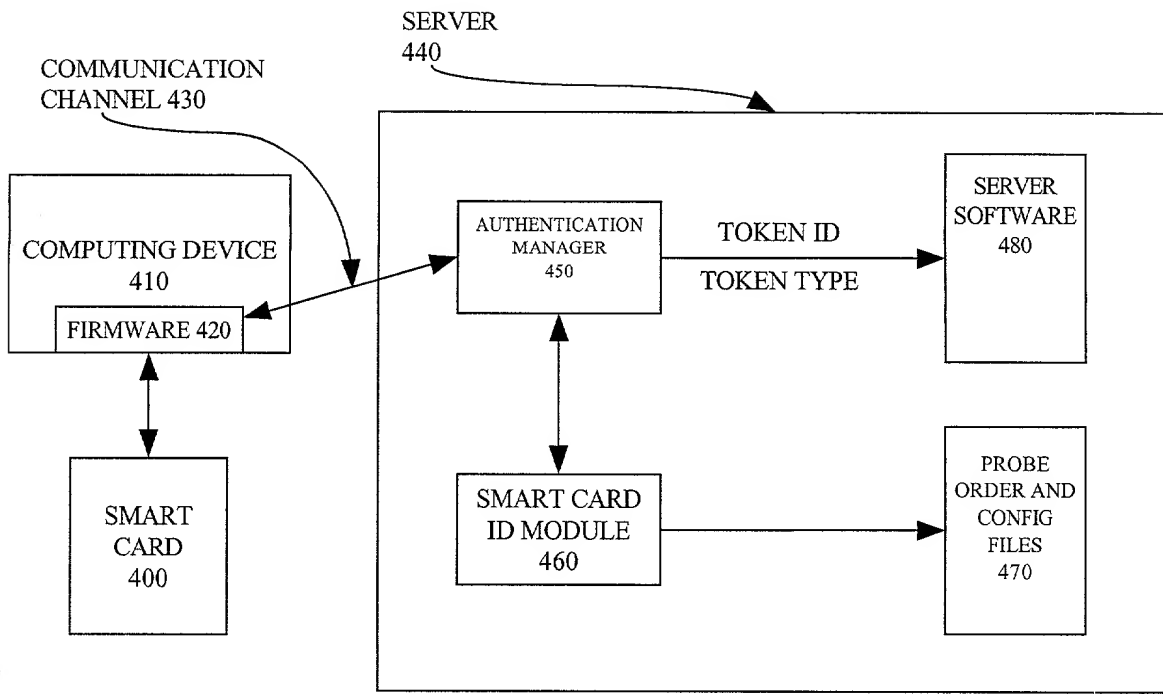


FIGURE 4

FIGURE 5

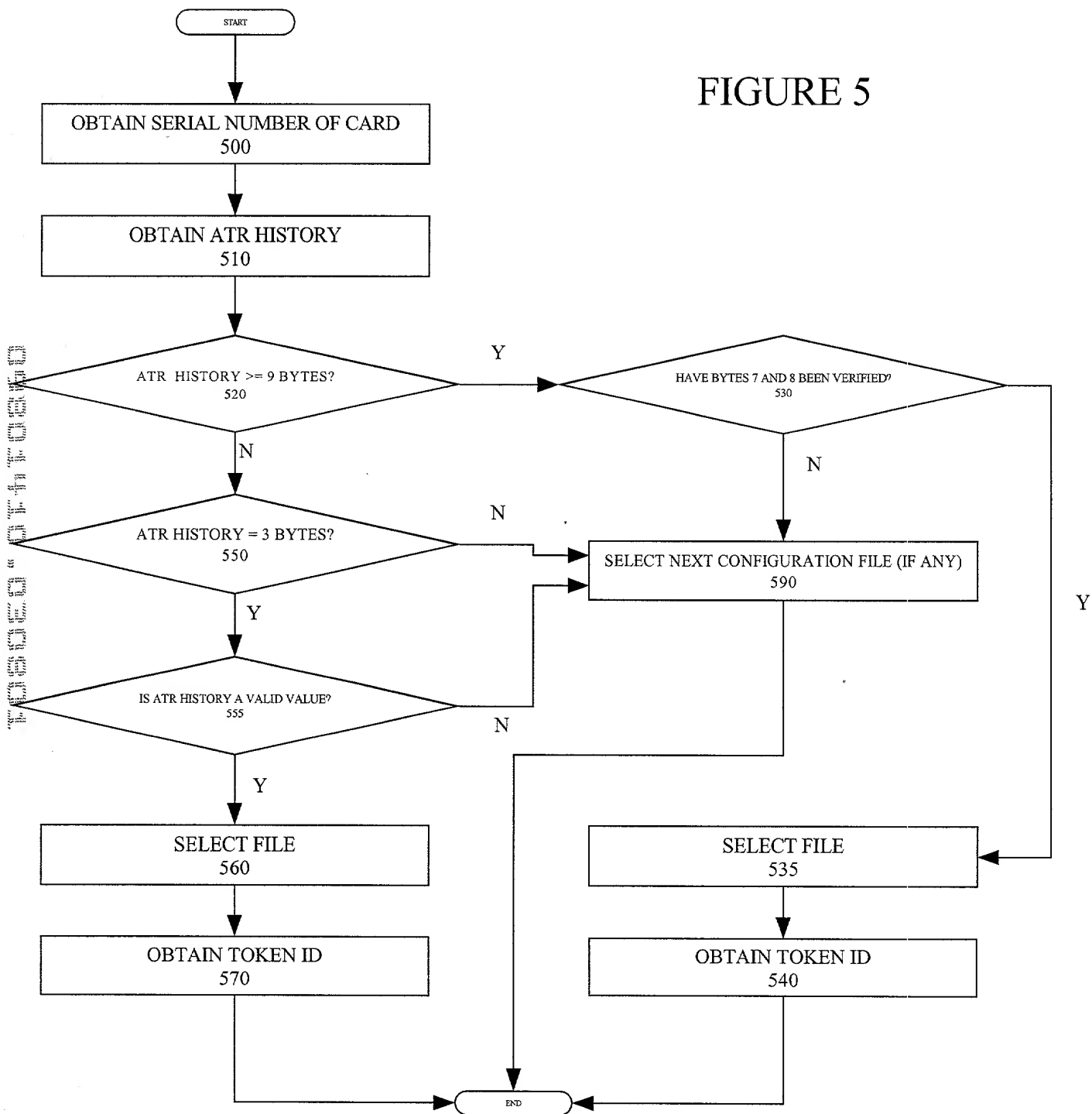
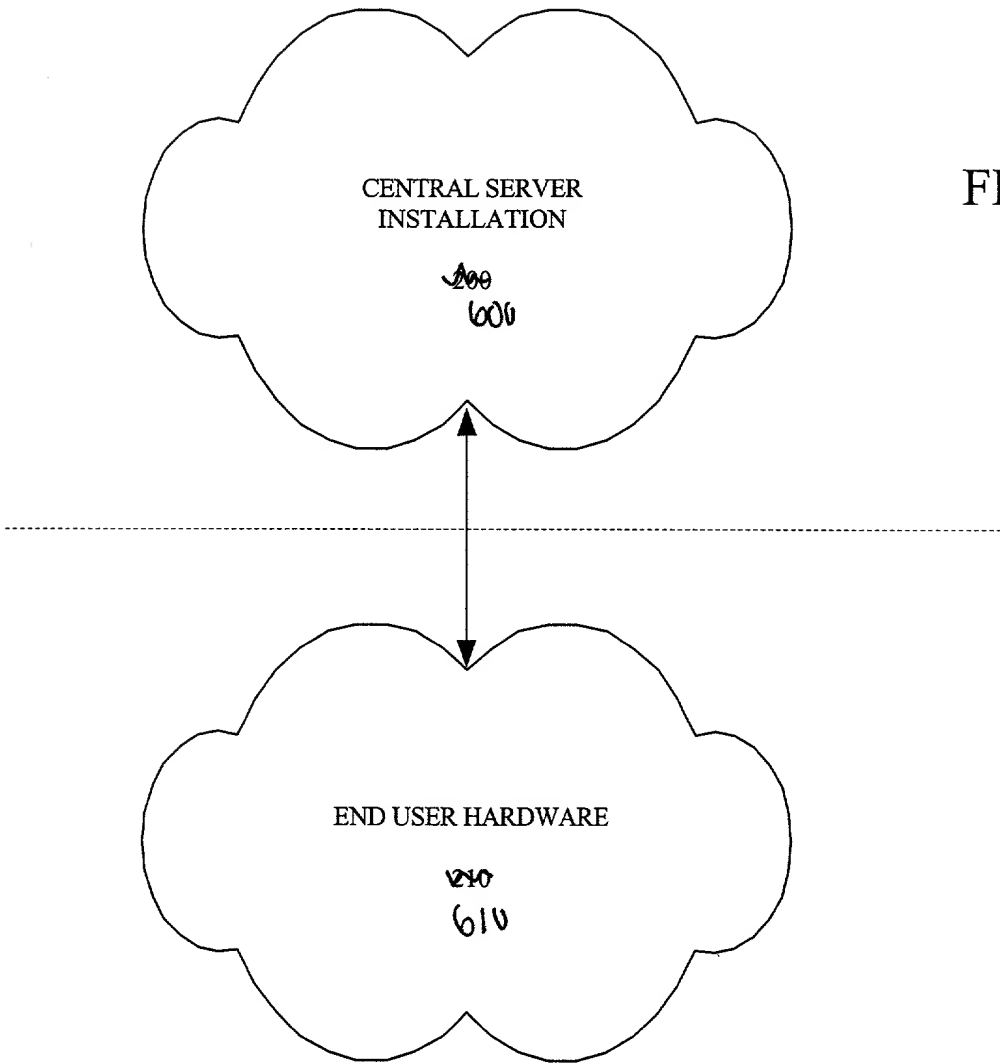


FIGURE 6



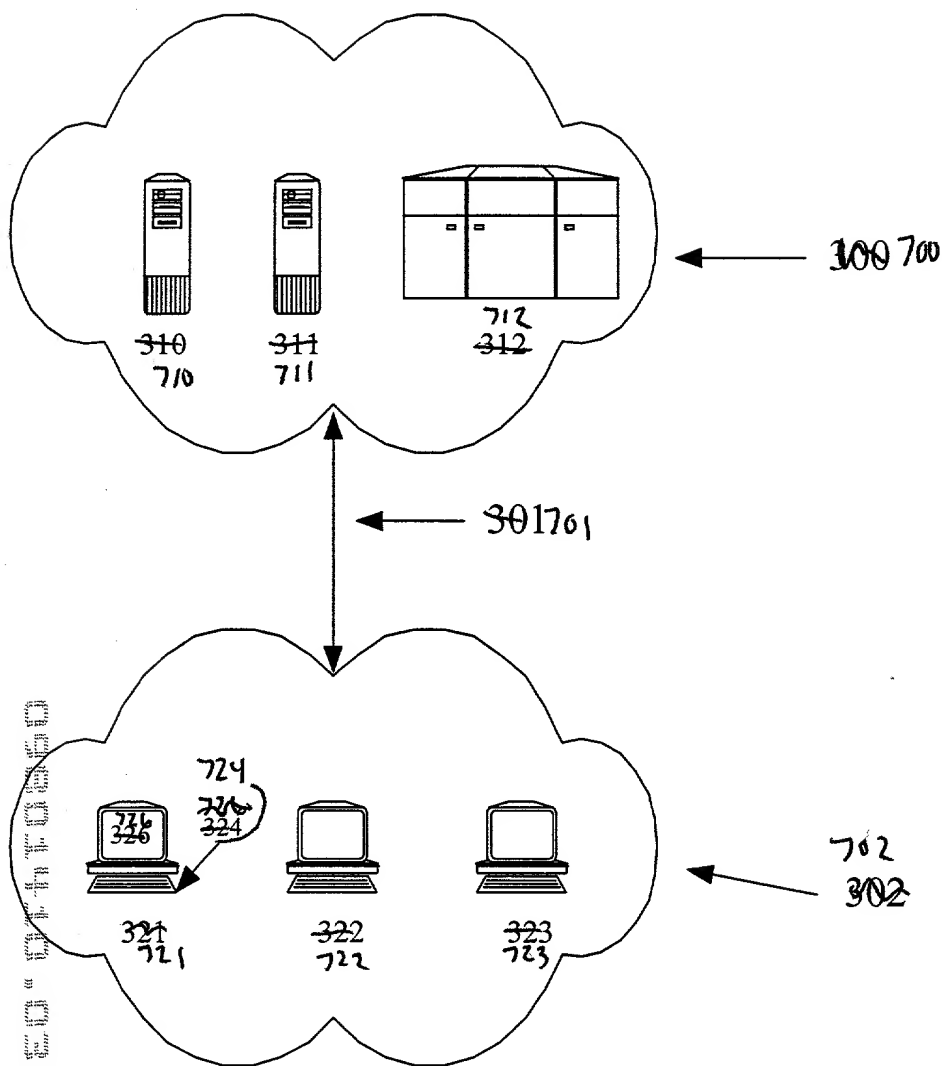
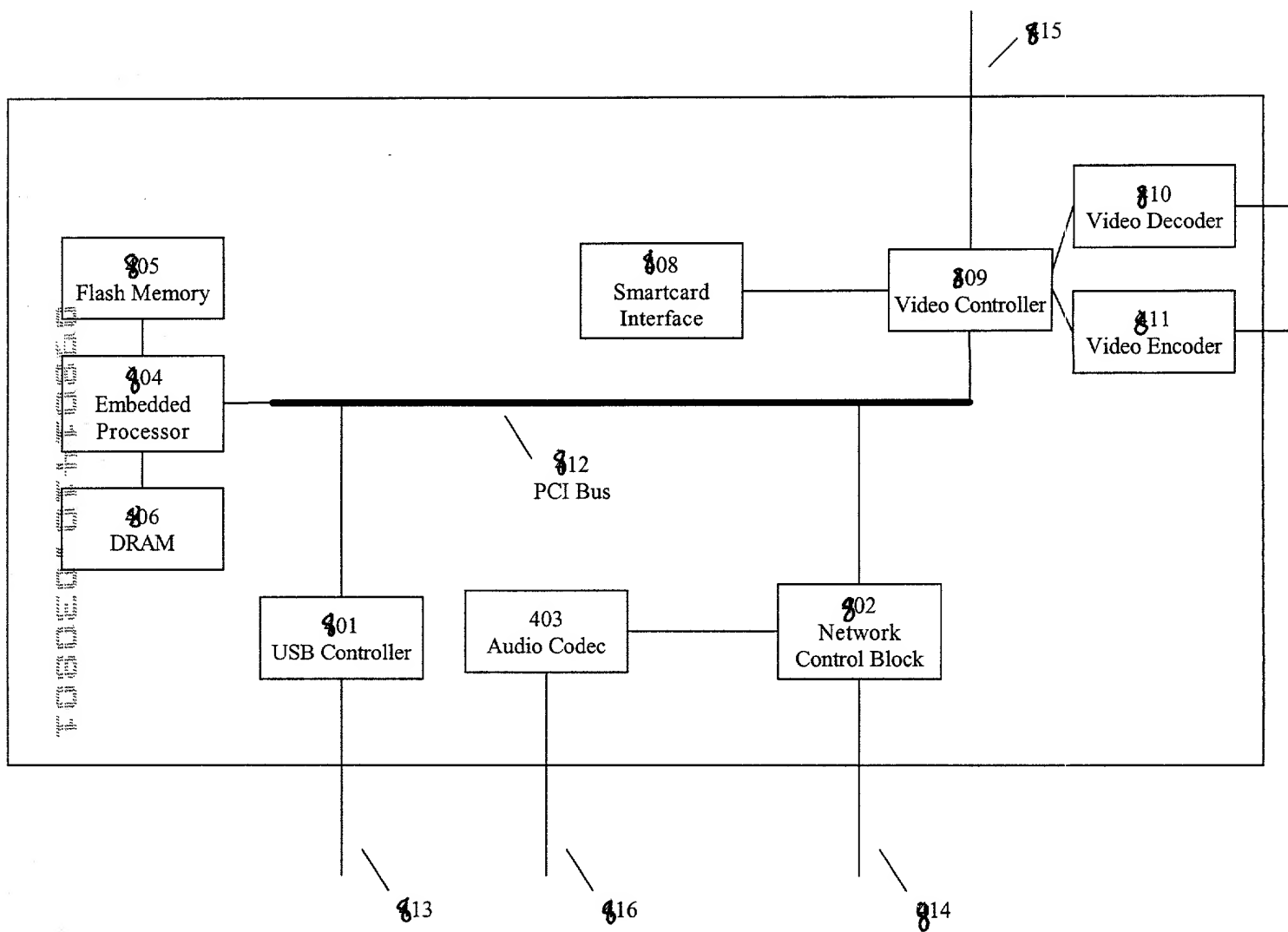


FIGURE 7





**Figure 4: HID**

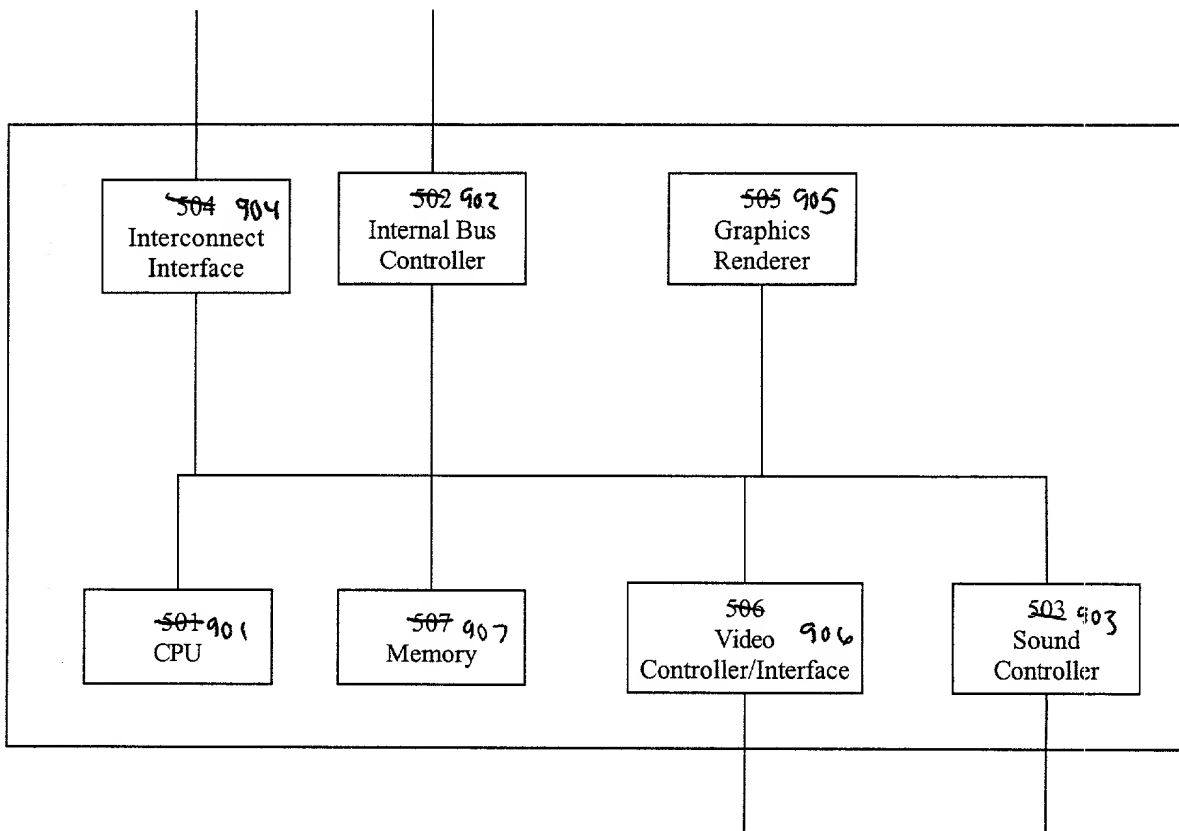


Figure 5: HID Single Chip Implementation

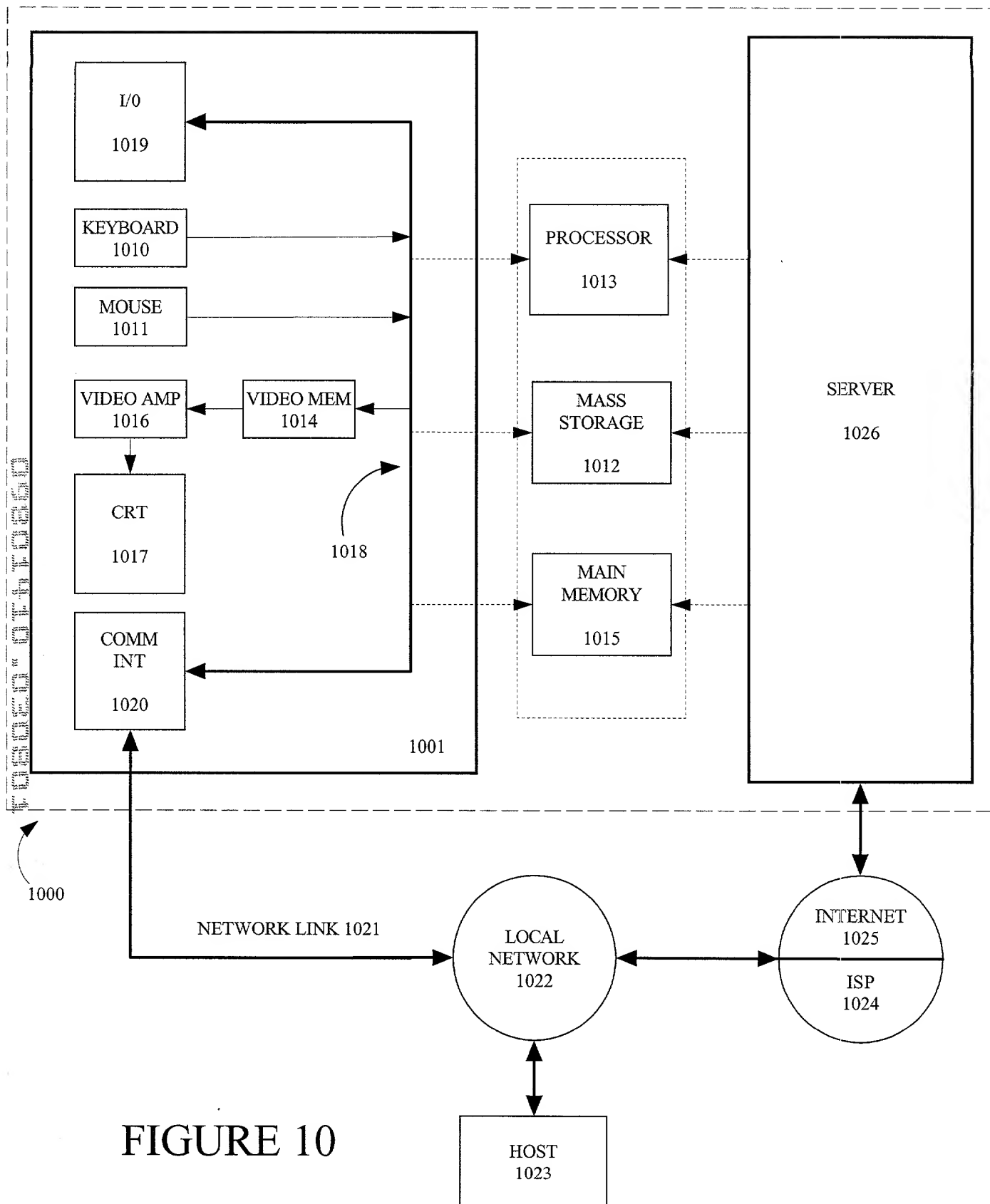


FIGURE 10